



501.40692X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): MIYAZAKI et al

Serial No.: 09/964,490

Filed: September 28, 2001

For: Fabrication Method Of Semiconductor Integrated Circuit Device

Group: 2812

Examiner:

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

January 22, 2002

Sir:

The following preliminary amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE SPECIFICATION:

Please replace the original specification with the attached Substitute Specification.

IN THE ABSTRACT OF THE DISCLOSURE:

Please replace the original abstract with the attached abstract.

RECEIVED
JAN 23 2002
TECHNOLOGY CENTER 2800